
Specification

for

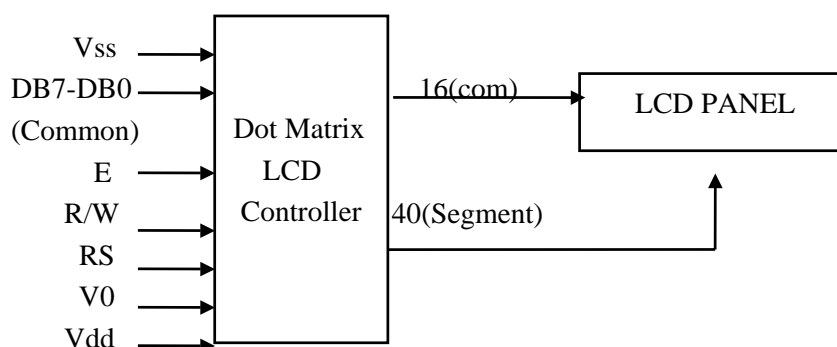
LCD Module

LH1610-1

5. PIN ASSIGNMENT

No.	Symbol	Level	Function
1	Vss	--	0V +5V for LCD
2	Vdd	--	
3	V0	--	
4	RS	H/L	Register Select: H:Data Input L:Instruction Input
5	R/W	H/L	H--Read L--Write
6	E	H.H-L	Enable Signal
7	DB0	H/L	Data bus used in 8 bit transfer
8	DB1	H/L	
9	DB2	H/L	
10	DB3	H/L	
11	DB4	H/L	Data bus for both 4 and 8 bit transfer
12	DB5	H/L	
13	DB6	H/L	
14	DB7	H/L	
15	BLA	--	BLACKLIGHT +
16	BLK	--	BLACKLIGHT -

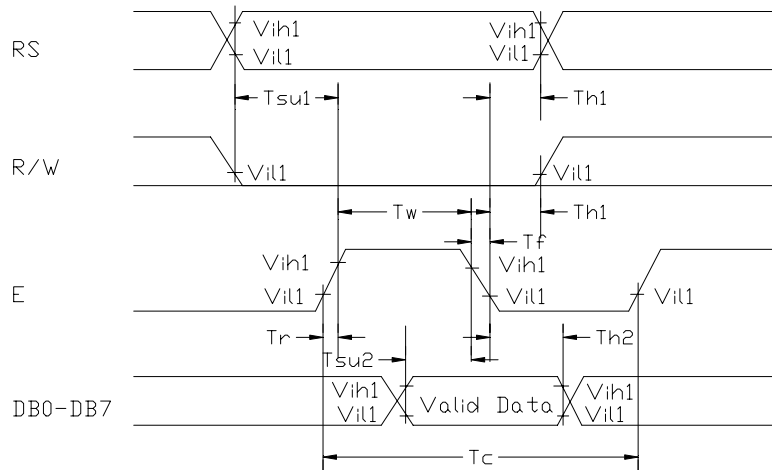
6. SYSTEM BLOCK DIAGRAM



7. AC characteristics (Vdd=5V±10%,Vss=0V Ta=25°C)

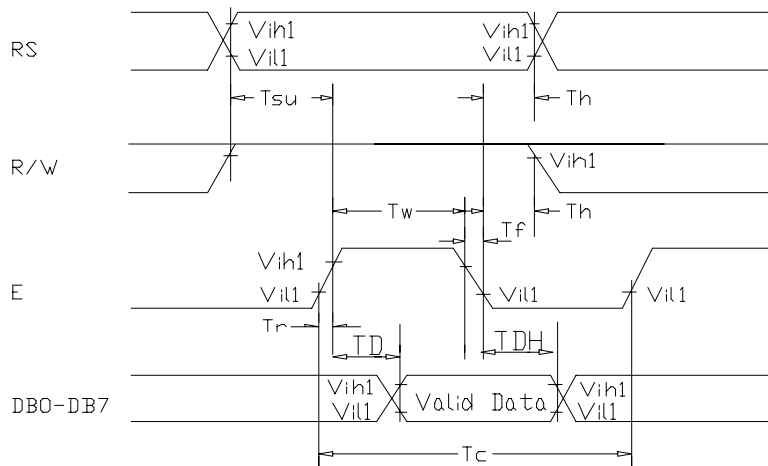
(1)Write mode(writing data from Micom to KS0070B)

Characteristic	Symbol	Min.	Tvp.	Max.	Unit	Test pin
E cycle time	t_c	500	--	--	ns	E
E rise time	t_r	--	--	25	ns	E
E fall time	t_f	--	--	25	ns	E
E pulse width (High,Low)	t_w	220	--	--	ns	E
R/W and RS set-up time	t_{su1}	40	--	--	ns	R/W,RS
R/w and RS hold time	t_{h1}	10	--	--	ns	R/W,RS
Data set-up time	t_{su2}	60	--	--	ns	DB0~DB7
Data hold time	t_{h2}	10	--	--	ns	DB0~DB7



(2) Read mode(Reading data from KS0066 to Micom)

Characteristic	Symbol	Min.	Typ.	Max.	Unit	Test pin
E cycle time	t_c	500	--	--	ns	E
E rise time	t_r	--	--	25	ns	E
E fall time	t_f	--	--	25	ns	E
E pulse width (High,Low)	t_w	220	--	--	ns	E
R/W and RS set-up time	t_{su1}	40	--	--	ns	R/W,RS
R/w and RS hold time	t_{h1}	10	--	--	ns	R/W,RS
Data set-up time	t_{su2}	--	--	120	ns	DB0~DB7
Data hold time	t_{h2}	20	--	--	ns	DB0~DB7



8. CONTROL and DISPLAY COMMAND

Command	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Remark
Display Clear	L	L	L	L	L	L	L	L	L	H	
Return Home	L	L	L	L	L	L	L	L	H	X	cursor move to first digit
Entry Mode Set	L	L	L	L	L	L	L	H	I/D	SH	I/D:set cursor move direction H-Increase L-Decrease SH:Specifies shift of display H-display is shifted L-Display is not shifted
Display On/Off	L	L	L	L	L	L	H	D	C	B	D:Display(H-on,L-off) C:Cursor(H-on,L-off) B:Blinking(H-on,L-off)
Shift	L	L	L	L	L	H	S/C	R/L	X	X	SC:(H-Display shift,L-Cursir move) R/L:(H-Right shift,L-Left shift)
Set Function	L	L	L	L	H	DL	N	F	X	X	DL:(H-8 bits interface,L-4 bits interface) N:(H-2 line display,L-1 line display) F:(H-5 x 10 dots,L-5 x 7 dots)
Set CG RAM Address	L	L	L	H	CG RAM address (corresponds to address)					CG RAM Data is sent and received after this setting	
Set DD RAM Address	L	L	H	DD RAM address					DD RAM Data is sent and received after this setting		
Read Busy Flag & Address	L	H	BF	Address Counter used for Both DD & CG RAM address					BF:(H-Busy ,L-Ready) --Reads BF indication internal operating is being performed --reads address counter contents		
Write Data	H	L	Write Data					Write data into DD or CG RAM			
Read Data	H	H	Read Data					Read data from DD or CGRAM			

■ ASSEMBLY DIAGRAM

